

Fig. 2
(Prior Art)

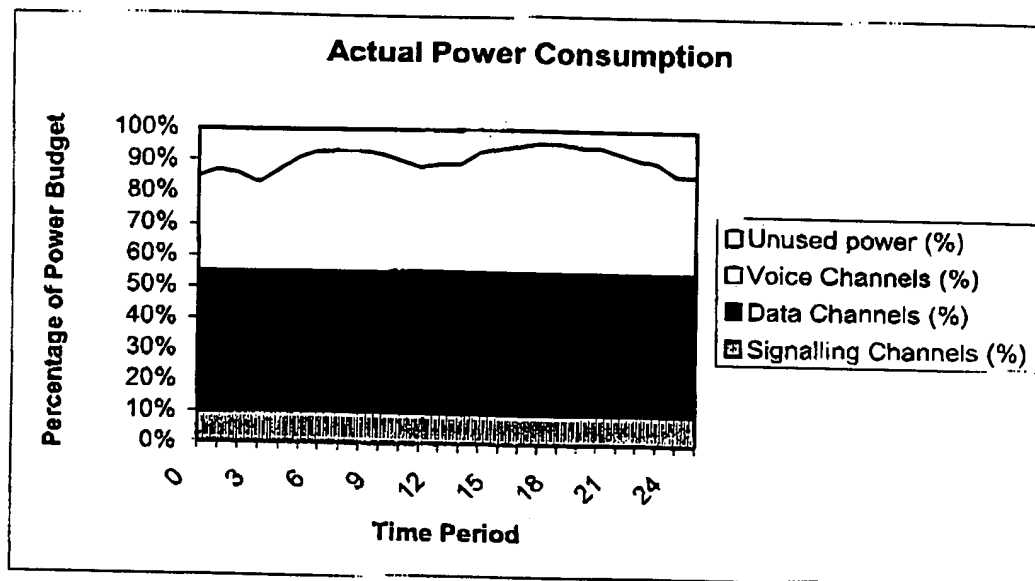


Fig. 3
(Prior Art)

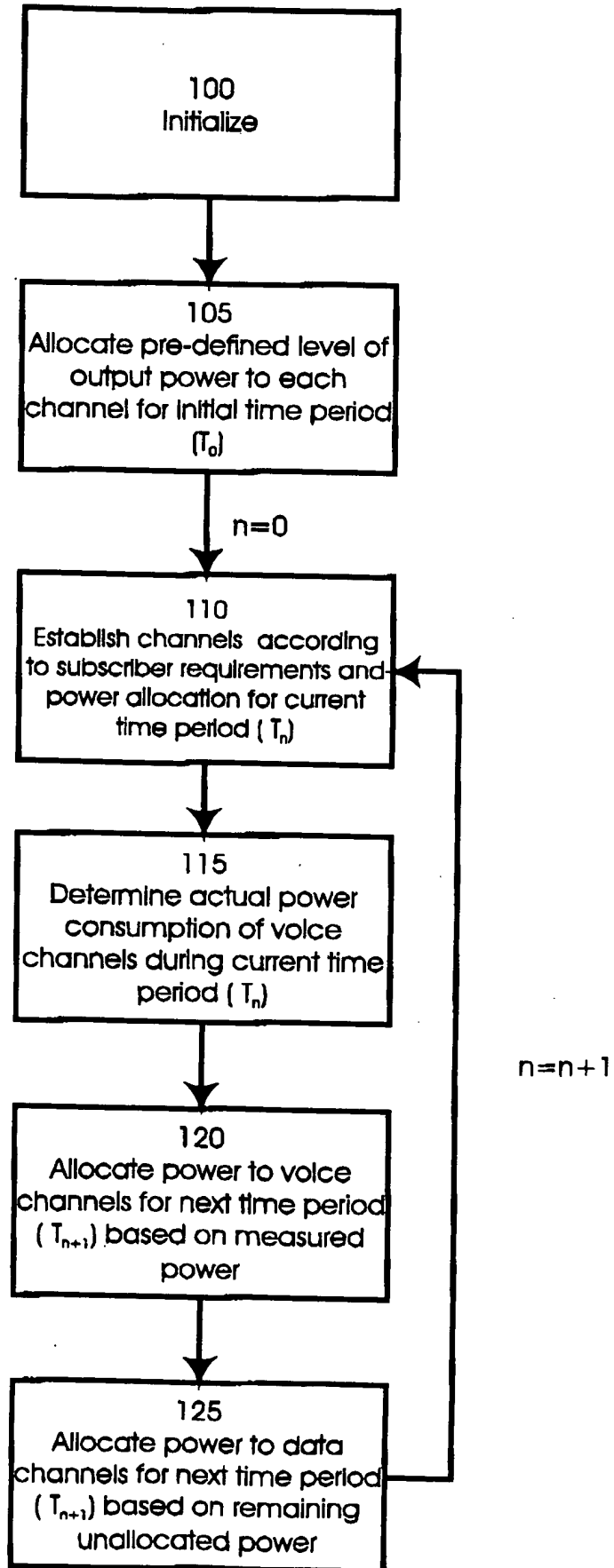
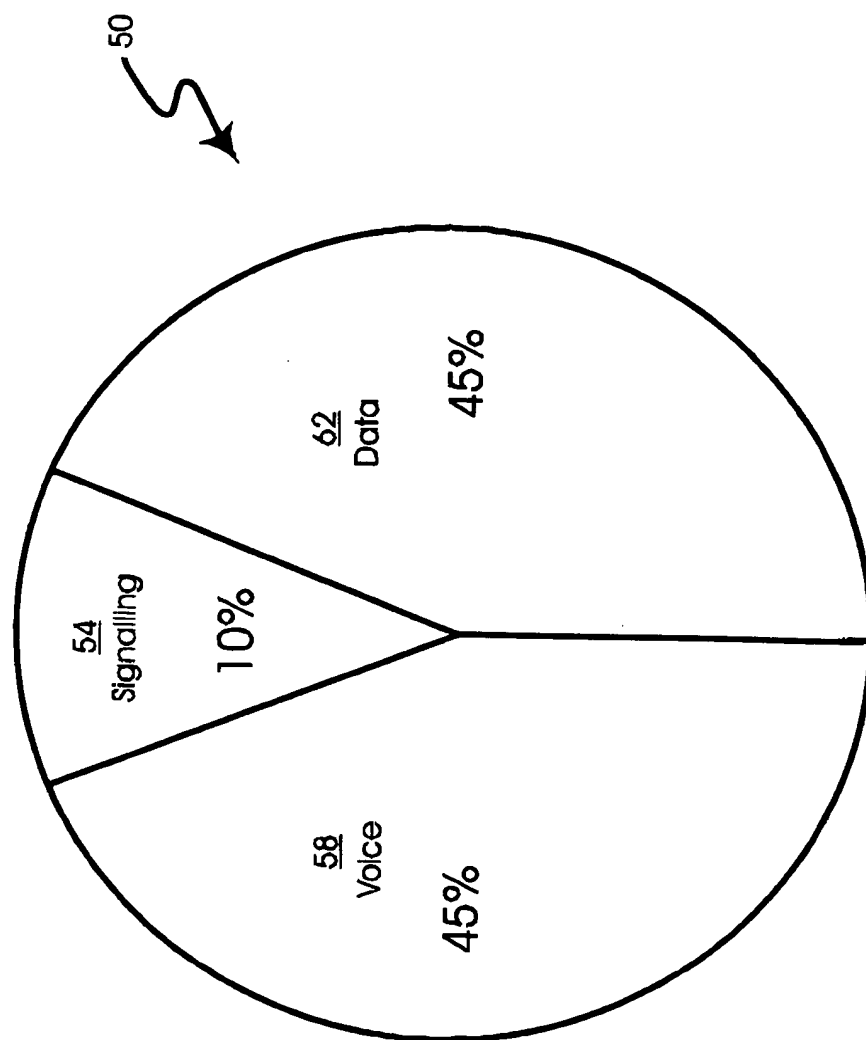


Fig. 4

Power Allocation
(T_0)

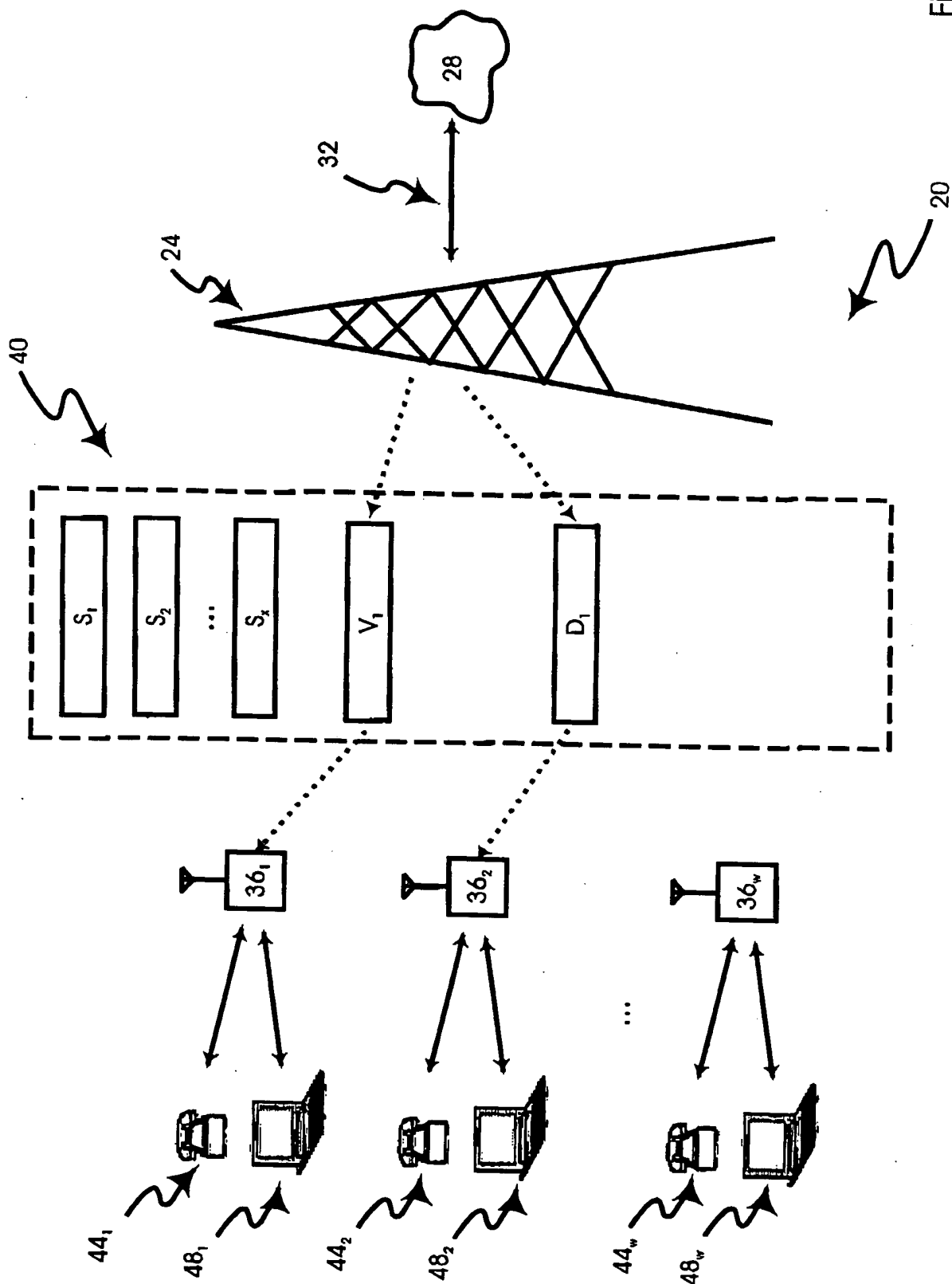
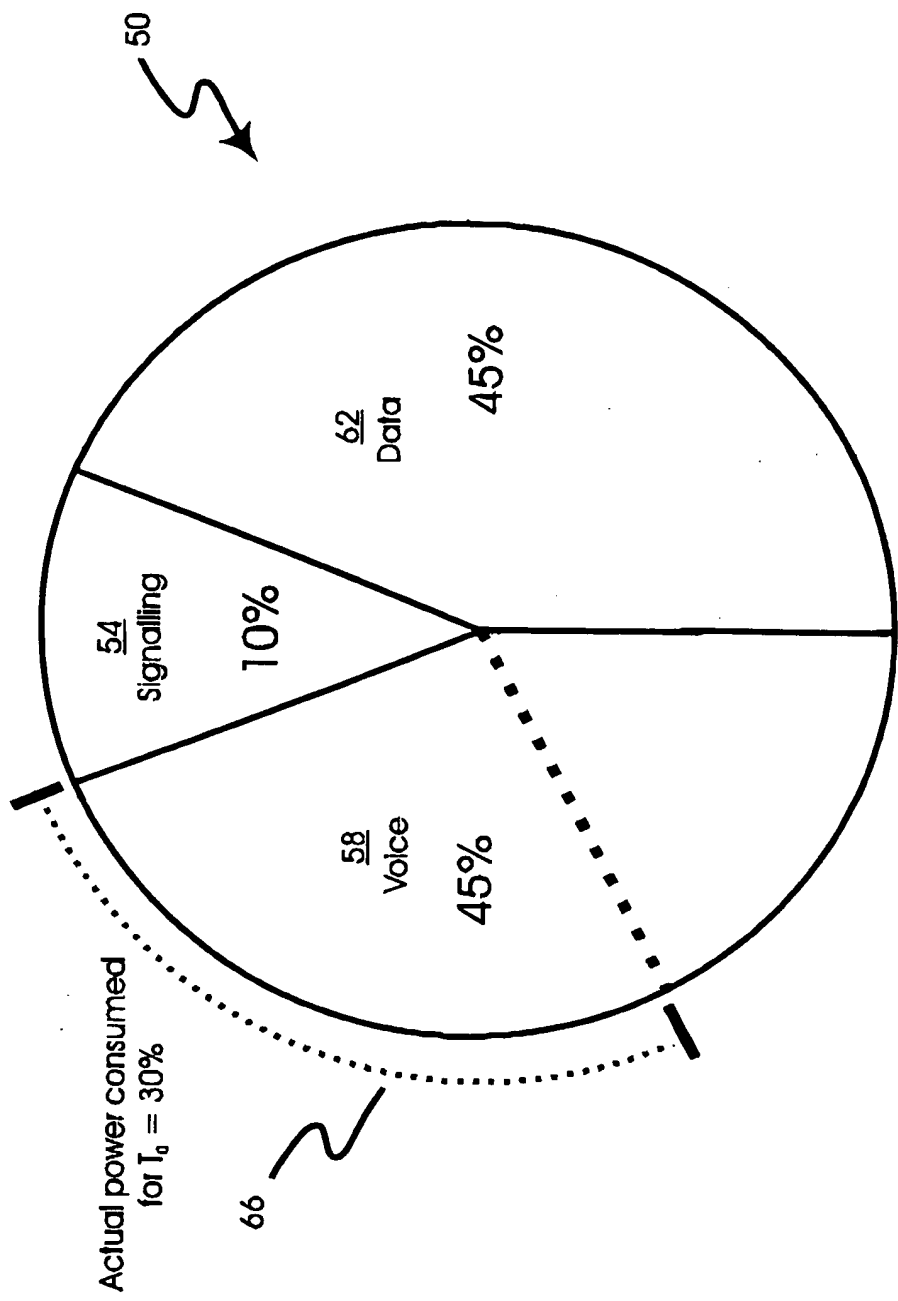


Fig. 6



Power Allocation (T_0)

Fig. 7

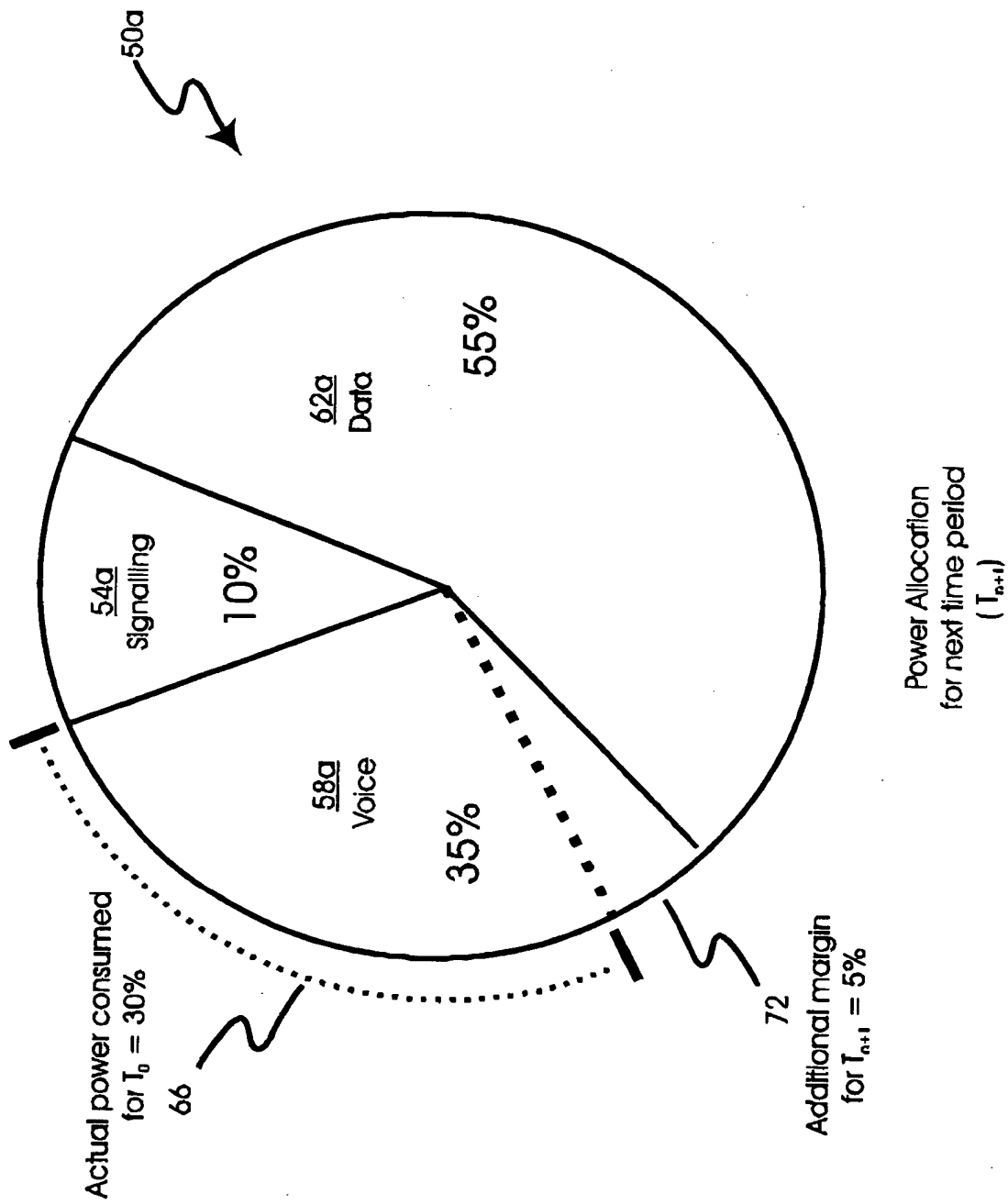


Fig. 8

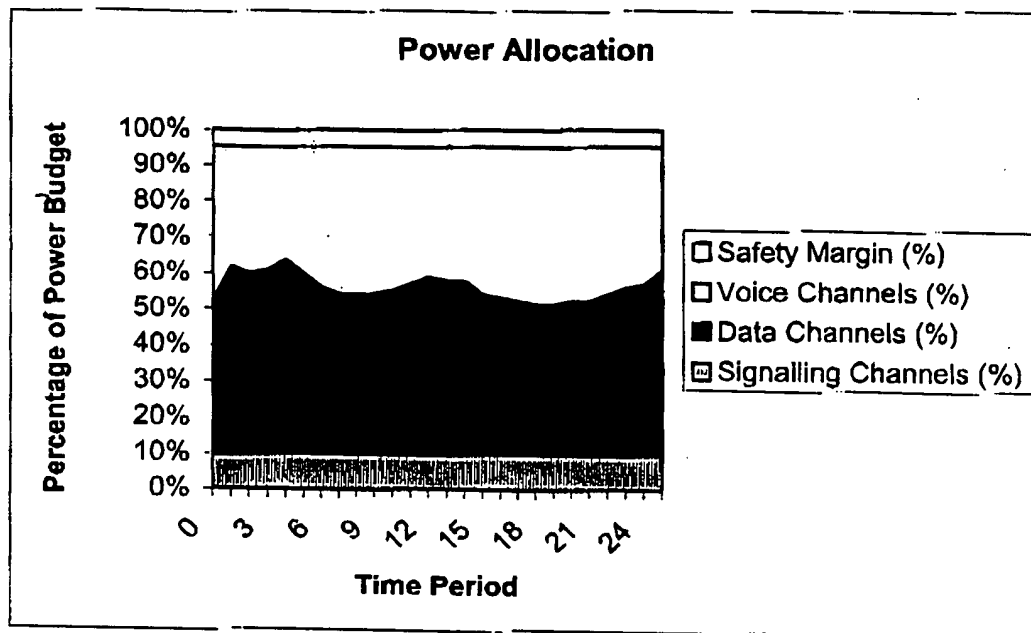


Fig. 9

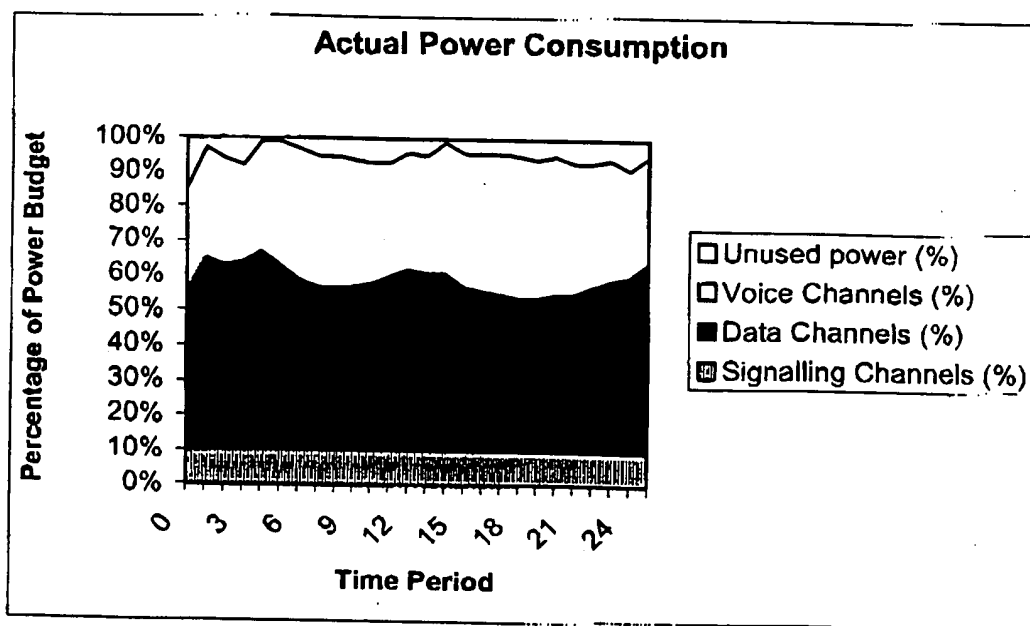


Fig. 10